ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

FES ID:

STRUCTURE AND METHOD OF FORMING BIPOLAR
TRANSISTOR HAVING A SELF-ALIGNED RAISED EXTRINSIC
BASE USING SELF-ALIGNED ETCH STOP LAYER

Submision Type: Utility Patent Filing

Application Number:

10/709220 59474

Server Response:

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First Named Applicant: Marwan Khater
Attorney Docket Number: FIS920030412US1

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